

## RISC-V 32-bit glibc port

*Wednesday, August 26, 2020 10:30 AM (30 minutes)*

This will include details about the 64-bit `time_t` problem and how RV32 is going to be the first 32-bit architecture with a 64-bit `time_t`. What still needs to be done for 32-bit support? How do we get this merged? We will also like to discuss the plan to test and maintain it once it is merged.

### **I agree to abide by the anti-harassment policy**

I agree

**Primary author:** FRANCIS, Alistair

**Presenter:** FRANCIS, Alistair

**Session Classification:** RISC-V MC

**Track Classification:** RISC-V MC