

X.Org Developer's Conference 2019

Wednesday, October 2, 2019

Main Track: Main track (7:55 AM - 5:40 PM)

time	[id] title	presenter
8:30 AM	[1] Opening Session	FILION, Mark
8:55 AM	[18] Zink: OpenGL on Vulkan	FAYE-LUND, Erik
9:50 AM	[38] Introducing the Vulkan WSI Layer	ZHELEV, Rosen Mr GARBETT, David Mr FRANCHIN, Matteo
10:20 AM	[50] ACO, a new compiler backend for GCN GPUS	NIEUWENHUIZEN, Bas SCHÜRMAN, Daniel
11:05 AM	Lunch	
12:40 PM	[24] How to not write a back-end compiler	EKSTRAND, Jason
1:35 PM	[34] Mediuimp support in Mesa	ROBERTS, Neil
2:20 PM	Coffee Break	
2:50 PM	[44] Implementing Optimizations In NIR	ROMANICK, Ian
3:45 PM	[52] Linux Graphics CI: Standardizing the kernel CI workflow and hardware, and improving our testsuites	PERES, Martin
4:10 PM	[17] Outreachy Internship Report: Refactoring backlight and spi helpers in drm/tinydrm	MADHYASTHA, Meghana
4:30 PM	Coffee Break	
4:50 PM	[64] Khronos Update & AMA	TREVETT, Neil

Thursday, October 3, 2019

Main Track: Main Track (7:55 AM - 7:00 PM)

time	[id] title	presenter
8:45 AM	[13] Opening Session	FILION, Mark
9:00 AM	[48] Monado: Open Source Augmented & Virtual Reality	HAAG, Christoph FERWERDA, Joey
9:55 AM	[28] KWin now and tomorrow	GILG, Roman
10:15 AM	Coffee Break	
10:45 AM	[40] Improving frame timing accuracy in Mesa, DRM and X	PACKARD, Keith
11:40 AM	[37] A case study on frame presentation from user space via KMS	FINK, Heinrich
12:25 PM	Lunch	
2:00 PM	[26] Nouveau Generic Allocator Implementation in GBM	JONES, James
2:55 PM	[42] Bulk moving mechanism on LRU for DRM/TTM	HUANG, Ray
3:15 PM	Coffee Break	
3:45 PM	[39] Lima driver status update	NUNES, Erico KHORUZHICK, Vasily ABBOTT, Connor
4:40 PM	[50] Freesync, Adaptive Sync & VRR	WENTLAND, Harry
5:35 PM	[25] Enabling 8K displays - A story of 33M pixels, 2 CRTCs and no Tears!	NAVARE, Manasi
6:00 PM	[65] X.Org Foundation Board of Directors Meeting	

Friday, October 4, 2019

Main Track: Main Track (8:45 AM - 5:45 PM)

time	[id] title	presenter
8:45 AM	[16] Opening Session	FILION, Mark
9:00 AM	[20] A whirlwind tour through the input stack development efforts	HUTTERER, Peter
9:55 AM	[23] Edging closer to the hardware for kernel CI on input devices	TISSOIRES, Benjamin
10:15 AM	Coffee Break	
10:45 AM	[25] Witchcraft Secrets from a Reverse Engineer	ROSENZWEIG, Alyssa
11:40 AM	[10] Everything Wrong With FPGAs	WIDAWSKY, Ben
12:25 PM	Lunch	
2:00 PM	[24] DRM/KMS for Android	DELVA, Alistair
2:25 PM	[21] Display hardware testing with Chamelium	SER, Simon
2:50 PM	[8] State of the X.org	VETTER, Daniel
3:10 PM	Coffee Break	
5:25 PM	[18] Closing session	STONE, Daniel