Contribution ID: **116** Type: **not specified**

RISC-V Container

Compared to VM, container technology has been always argued for the security. We might need to discuss how to fit current container implementation into RISC-V arch in such a area. And RISC-V has not had any particular hardware considerations like Intel SGX and even AMD, however we can go far as we can and get some feedback to RISC-V foundation.

I agree to abide by the anti-harassment policy

Yes

Primary author: CHEN, Tiejun (VMware)

Presenter: CHEN, Tiejun (VMware)

Session Classification: RISC-V MC