



Contribution ID: 244

Type: **not specified**

RISCV NOMMU/M-Mode Linux

Monday, 9 September 2019 13:15 (15 minutes)

This presentation will discuss the work ongoing to implement Linux kernel support for RISCV hardware lacking a memory management unit (MMU). A side effect of this work is also the ability to execute the kernel directly in M-Mode and how this is implemented while keeping most of the architecture code unmodified.

The presentation will include examples of testing environment builds, discuss the support state of userspace toolchains and C libraries and will present the direct application of this work to a real hardware platform (Kendryte K210 SoC).

I agree to abide by the anti-harassment policy

Yes

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Session Classification: RISC-V MC