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Introduce an implementation of perf trace in riscv system

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RISC-V trace spec draft have defined some trace format, we'll share our implementation of linux perf trace based on the spec. How to deal with SMP perf issues, how to verify our design in qemu, demonstrate a demo of perf trace with riscv-qemu.

Lastly, let's discuss perf issues from PMU to trace, any riscv perf topic.

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Yes

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