



Contribution ID: 242

Type: **not specified**

RISC-V hypervisor Spec - The Good, the Bad and the Ugly

Thursday, November 15, 2018 11:30 AM (30 minutes)

The RISC-V ISA is still missing a key aspect in modern computing by not having virtualization support. The spec is currently in draft state, although most of the key elements are there. We can discuss what the next steps are in order to start getting hypervisors running, at least in QEMU. We can also discuss having the spec ratified and included in the official RISC-V ISA.

I agree to abide by the anti-harassment policy

Presenter: FRANCIS, Alistair (Western Digital)

Session Classification: RISC-V MC