Contribution ID: **196**   Type: **not specified**

Power Sequencing for Enumerable Busses - What's Still Missing From the Device Tree and Kernel

On x86 / ACPI platforms, devices on enumerable busses can normally be seen directly by the OS. On DT platforms, these devices sometimes require extra power sequencing like toggling regulator supplies or GPIO lines. Over the years most of these cases have been solved, but there are still some gaps.

This talk will go through what already works, and attempts to identify:
- What's missing from the device tree
- What's defined in the device tree, but doesn't really work in the kernel
- What kludges we have accumulated and haven't cleaned up

Primary author: TSAI, Chen-Yu (Google, LLC)

Presenter: TSAI, Chen-Yu (Google, LLC)

Session Classification: Devicetree MC

Track Classification: Devicetree MC