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Supporting multiple power domains in clock controllers

The core kernel platform code will automatically switch on a power-domain associated with a device.

Some devices have multiple power-domains and in such cases are typically expected to manage those multiple power-domains themselves.

For clock controllers there are several ways to attack this problem.

- Bury it inside of the clock on/off path in a clock arch specific way
- Bury it inside of the platform clock driver with pm runtime calls managing the switching on/off of power-domains
- Some other generic solution

A proposed discussion on whether this problem is generic enough to fix generically and if so how.

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