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An introduction to the DPLL subsystem

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More and more systems require precisely synchronized time to operate effectively. NTP and PTP are well-known protocols that distribute time information across networks. However, systems need hardware to be properly configured and monitored as a part of the solution. With SyncE adoption there is a requirement to support more rigorous approaches to time synchronization and distribution on hosts. One method is to use Digital Phase Lock Loop (DPLL) circuits to syntonise frequencies over the board. This new subsystem provides an API to configure and monitor such hardware in Linux using netlink as a transport protocol.

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