



Contribution ID: 205

Type: **not specified**

CXL Memory Tiering for heterogenous computing

Monday, 13 November 2023 17:10 (25 minutes)

Compute Express Link (CXL), an open-standard interconnect protocol, overcomes architectural limitations by efficiently expanding memory capacity and bandwidth. Linux enables interleave between various memory tiers. Current presentation provides workload analysis with various memory tiers to improve system performance. Presentation provides various Hardware and Linux kernel configurations to tune system for various workloads which are sensitive to bandwidth and capacity. Also propose and discuss various solutions for Application transparent mechanism for improving system performance.

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Session Classification: Compute Express Link MC

Track Classification: LPC Microconference: Compute Express Link MC