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CXL Emulation in QEMU - Progress, status and most importantly what next?

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CXL continues to be a fast evolving standard, with upstream kernel and tooling support often running ahead of mass availability of hardware.

From the early days of CXL, QEMU has been used for CXL system emulation. The emulated feature set continues to expand to support more advanced features, with a steadily growing group of contributors, many of whom contributed to the discussions last year.

Last year's session focused on introducing the reasoning behind emulating CXL in QEMU, and the status at that time.

This year, after a very brief status update, the first half of the session will focus will be on currently hot topics, likely to include some of:

- Multithreaded devices (MHDs)
- Dynamic Capacity (memory pooling)
- Fabric Management
- Type 2 Devices (accelerators)

The second part of the session will be open discussion with the aim of capturing feedback on which features are of particular interest for the coming year, perhaps addressing needs called out elsewhere in the uconf.

Of interest to: CXL kernel developers, Fabric management / BMC software developers, those looking at CXL applications, those interested in use of QEMU to support ecosystem development.

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