Linux Plumbers Conference

Richmond, Virginia | November 13-15, 2023

CXL beyond memory

Agenda

- General interest?
- Type 1 vs Type 2
- Current work
  - kernel branch
  - qemu

Backup

- Give more time to DCD…
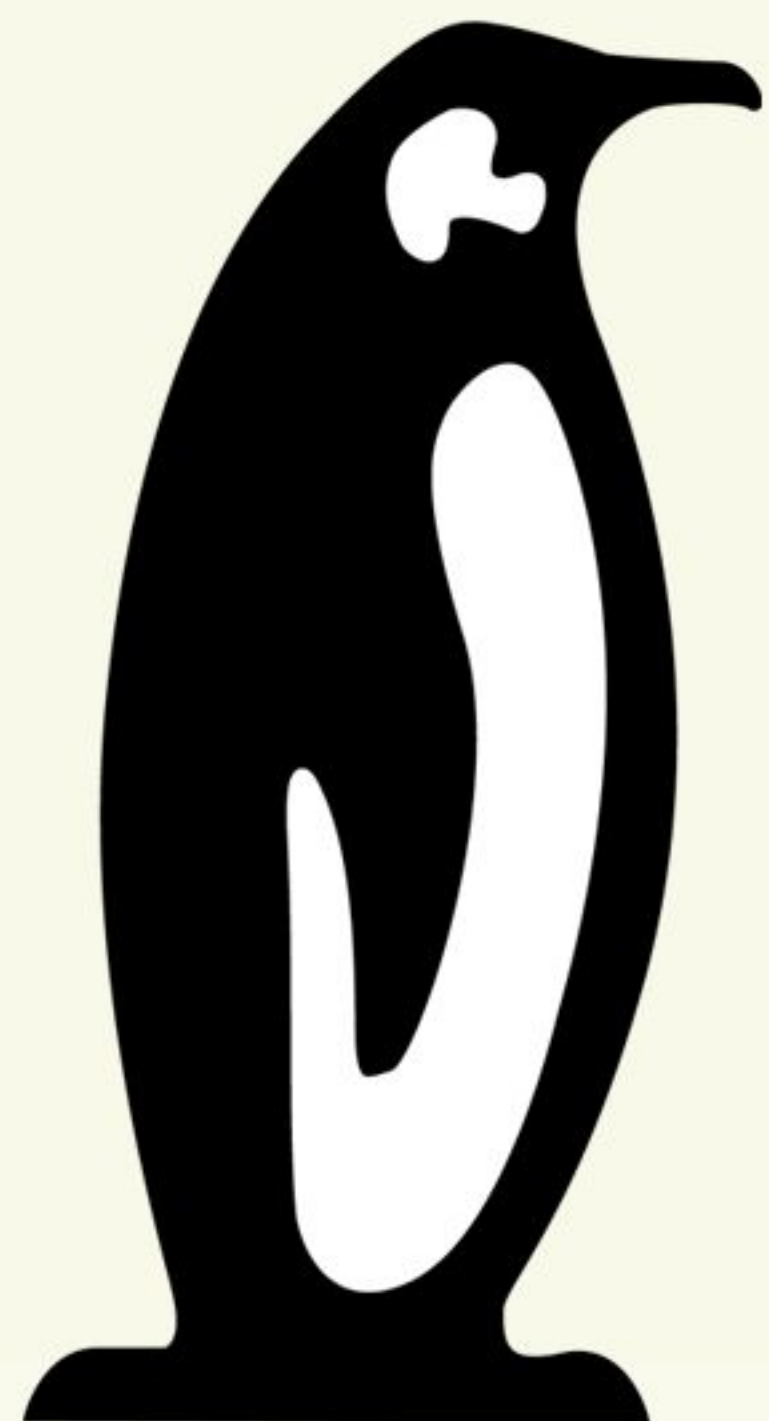
Type 1 vs Type 2

- Both describe accelerator devices
- Type 1 has direct access to main memory
  - CXL.cache
- Type 2 has both direct access and on board memory
  - CXL.cache
  - CXL.mem

Current work

- Kernel
  - Dan's RFC work
  - cxl_test
- QEMU
  - Designed to test core checks
  - Derive an accelerator device from CXL.mem
  - How far should this go?

Linux
Plumbers
Conference

Richmond, Virginia | November 13-15, 2023