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RISC-V MC BoF

Wednesday, 14 September 2022 17:00 (1h 30m)

RISC-V community needs several topics to be discussed. Some of them will be discussed during MC but the allotted time may not be sufficient. We will probably some more discussion time. Here are some potential topics.

1. ACPI patch acceptance policy – ECR approval, RVI freeze or ACPI release (may significantly delay things)
2. Vendor SBI extensions
   – There are few vendor extensions patches appeared in the mailing list. There is no freeze criteria there. Can we just treat them as regular vendor patches or there are blockers for them ?
3. User space access to all ISA extensions
   - /proc/cpuinfo shows the ISA string - Is that sufficient ?
   - How to show extension specific additional meta data ? e.g. cache block size in Zicbom ?
4. User space access to vendorid, impiid, archid
5. /proc/cpuinfo and/or syscall or any other mechanism
6. Any other ?

I agree to abide by the anti-harassment policy

Yes

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