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PCIe Endpoint Subsystem Open Items Discussion

Monday, September 12, 2022 11:15 AM (30 minutes)

PCIe Endpoint Framework is a relatively new framework added to Linux Kernel. There are two upstreamed generic function drivers added; one for PCIe Endpoint Test (simple test function to test the communication between root-complex and endpoint) and the other for providing NTB functionality to the host (two endpoints within SoC facilitate two hosts to communicate).

A new endpoint function was added that kind-of hacks around to use NTB framework for PCIe RC to EP communication. While this provided network interface between RC and EP, is this the right way to do? Should we build on PCIe VirtIO standard or allow ad-hoc drivers?

Other Open Items to discuss:

- 1) PCIe Endpoint Notifier
- 2) Moving to genalloc for outbound window memory allocation
- 3) Interrupts handling in EP mode
- 4) Device Tree Integration

I agree to abide by the anti-harassment policy

Yes

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