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## **Dynamic MSI-X and IMS opens**





- Enable allocation of MSI-X after probe phase (no longer one shot)
- New API: int pci\_add\_msix\_irq\_vector (struct pci\_dev \*dev)
- Should be called only after pci\_alloc\_irq\_vectors() or variants

Patches: https://github.com/intel/idxd-driver/tree/intel\_dynamic\_msix





1. Support multiple formats for interrupt messages

2. IMS PASID programming