BoF: Register pressure sensitivity in the gcc middle end

Thursday, 23 September 2021 10:15 (45 minutes)

There are a number of optimizations done in the middle end that would benefit from understanding the amount of register pressure. Unrolling, inlining, and parallel reassociation are some that come to mind immediately. I think it would be good to have a discussion about how these optimizations might get pressure information to know how aggressive they should be.

I agree to abide by the anti-harassment policy

I agree

Primary author: SAWDEY, Aaron (IBM)
Presenter: SAWDEY, Aaron (IBM)
Session Classification: GNU Tools Track
Track Classification: GNU Tools Track