

BoF: Register pressure sensitivity in the gcc middle end

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There are a number of optimizations done in the middle end that would benefit from understanding the amount of register pressure. Unrolling, inlining, and parallel reassociation are some that come to mind immediately. I think it would be good to have a discussion about how these optimizations might get pressure information to know how aggressive they should be.

I agree to abide by the anti-harassment policy

I agree

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